FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. MIC-40	APPLICATION NO. 10/722,959
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	APPLICANT Seong-Hoon Lee	CONFIRMATION NO. Not Yet Assigned
	FILING DATE November 26, 2003	GROUP 2816 Not Yet ~~Assigned~~

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
AK	5,463,337	10/31/95	Leonowich	327	158	
AK	6,194,947 B1	02/27/01	Lee et al.	327	359	
AK	6,295,328 B1	09/25/01	Kim et al.	375	376	
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

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AK	Jong-Tae Kwak, A Low Cost High Performance Register-Controlled Digital DLL for 1 Gbps x32 DDR SDRAM, The 8th Korean Conference on Semiconductors, February 2001.
AK	Ramin Farjad-Rad, A Low-Power Multiplying DLL for Low-Jitter Multigigahertz Clock Generation in Highly Integrated Digital Chips, IEEE Journal of Solid-State Circuits, Vol. 37,. No. 12, December 2002, p. 1804-1812.

All references have been considered. AK
EXAMINER INITIAL

EXAMINER LUU DATE CONSIDERED 11/23/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not conformance and not considered. Include copy of this form with next communication to applicant.